

100

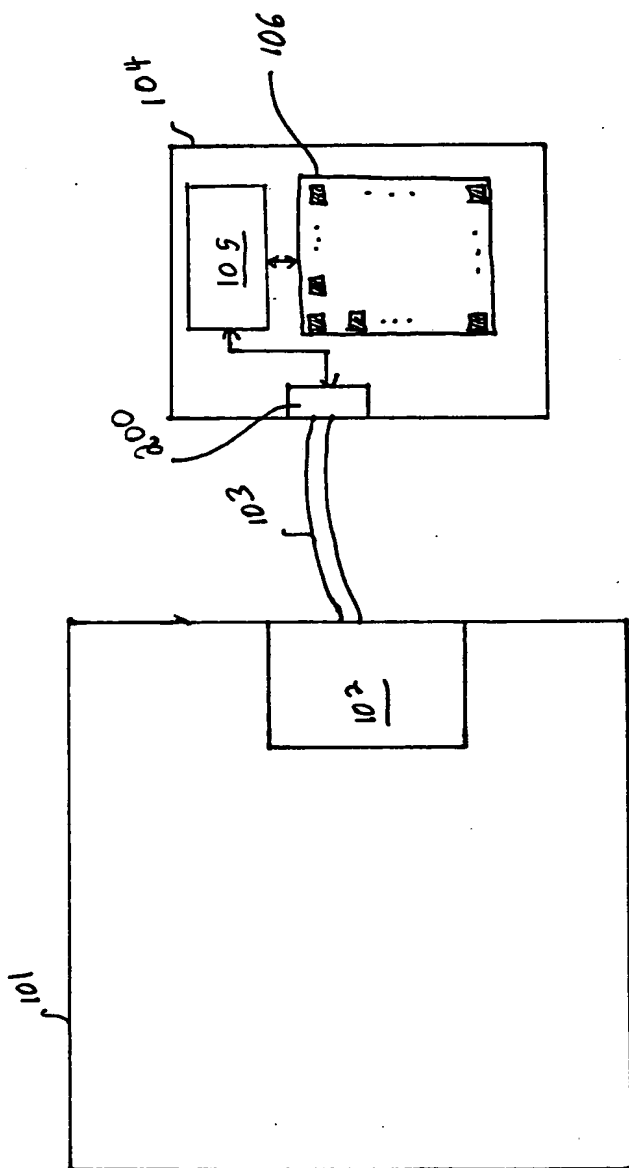


Figure 1

# Block Diagram

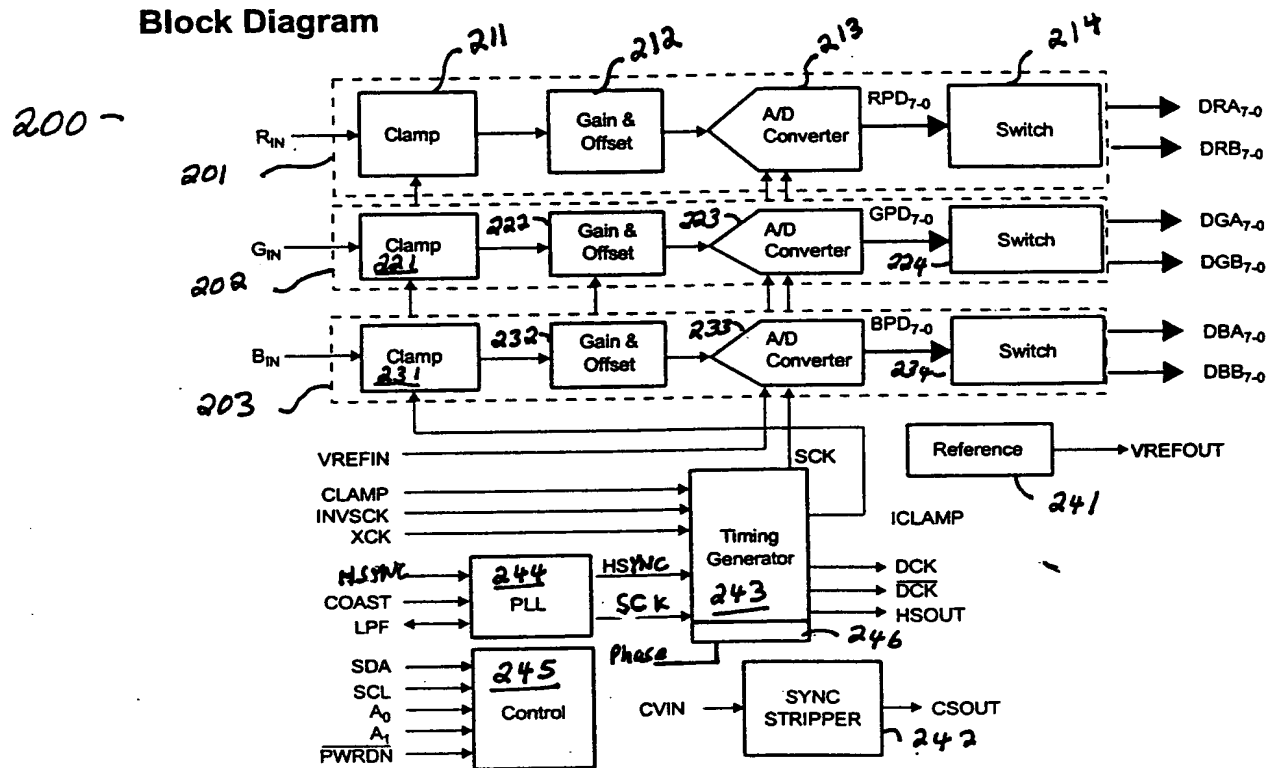


Figure 2a

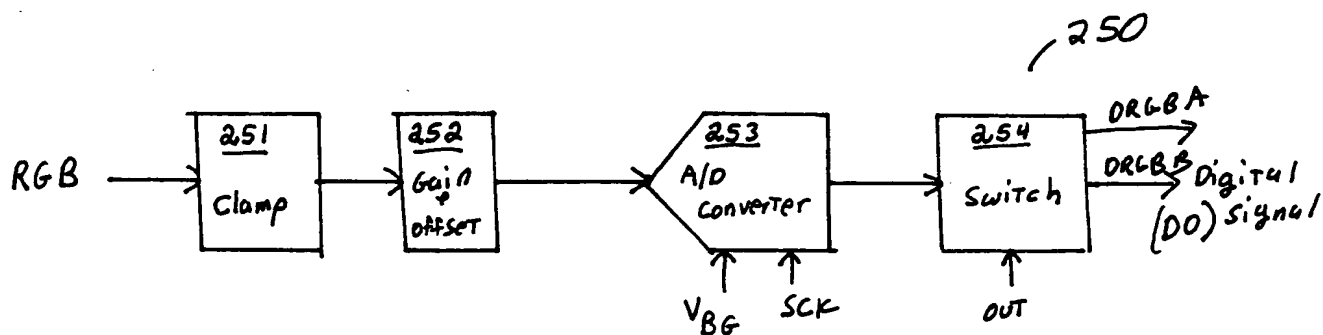


Figure 2b

Figure 2c

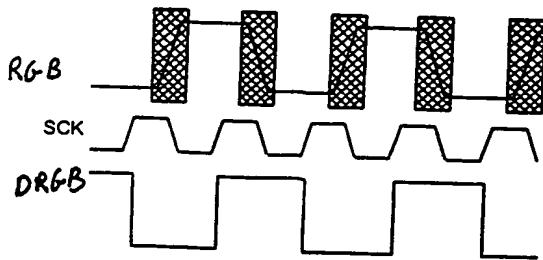
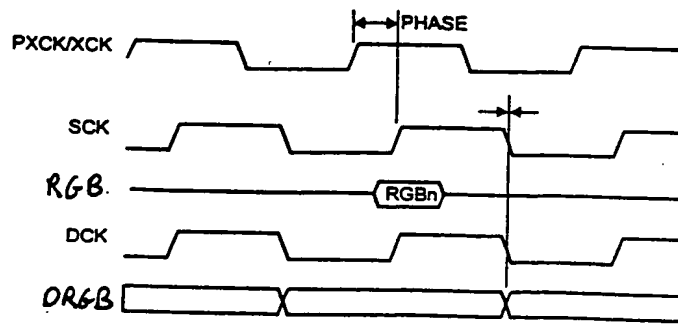


Figure 2d

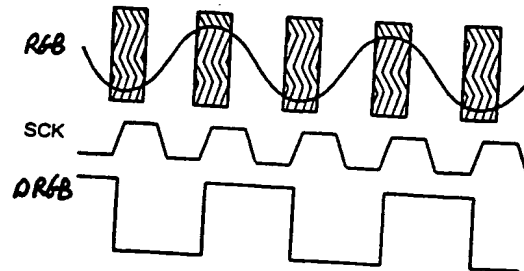


Figure 2e

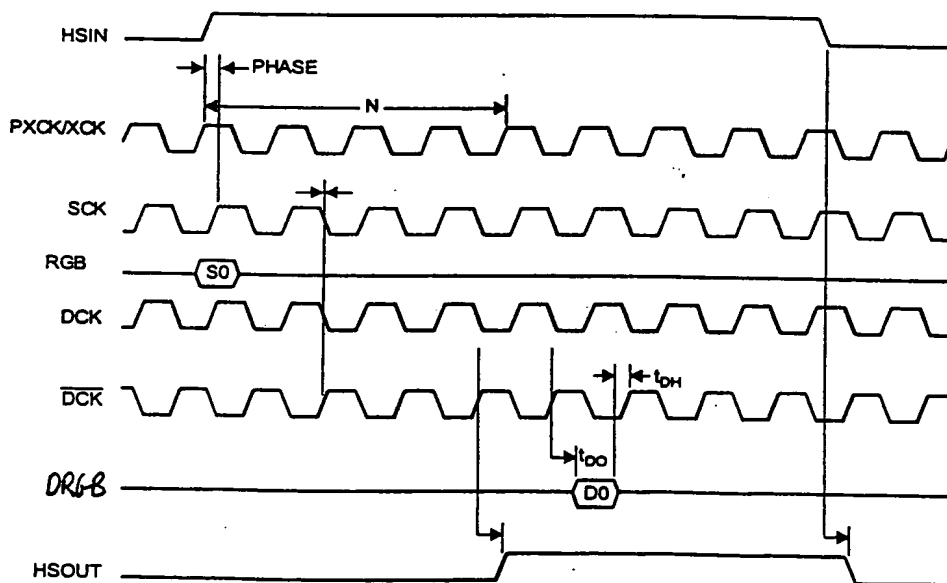


Figure 2f

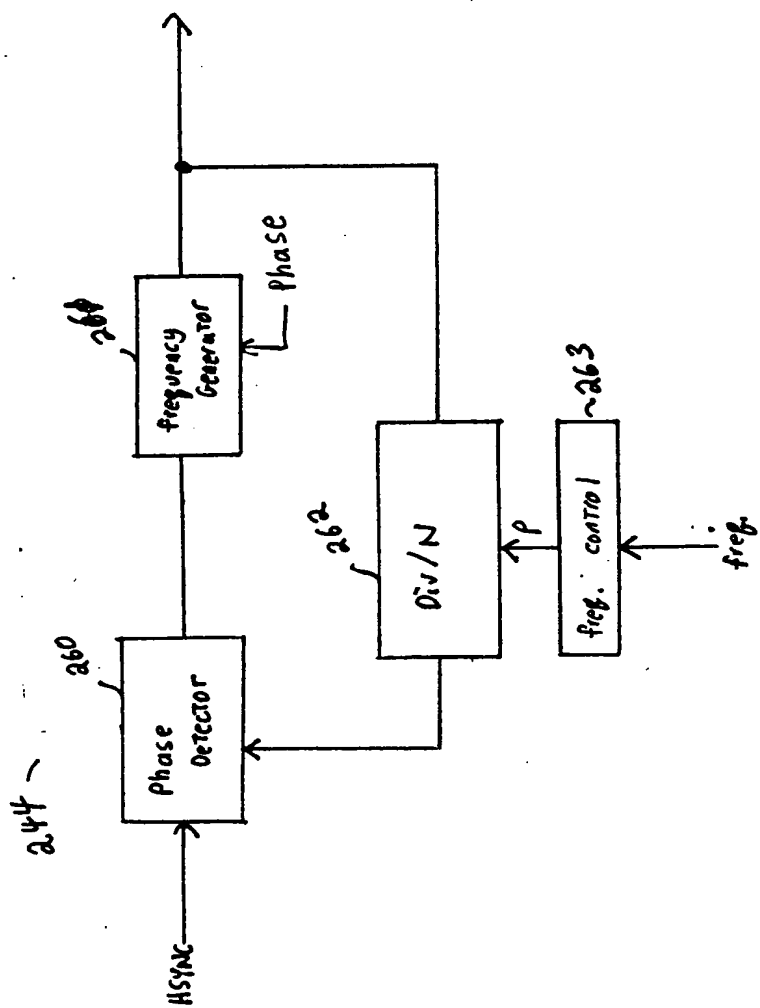


Figure 2g

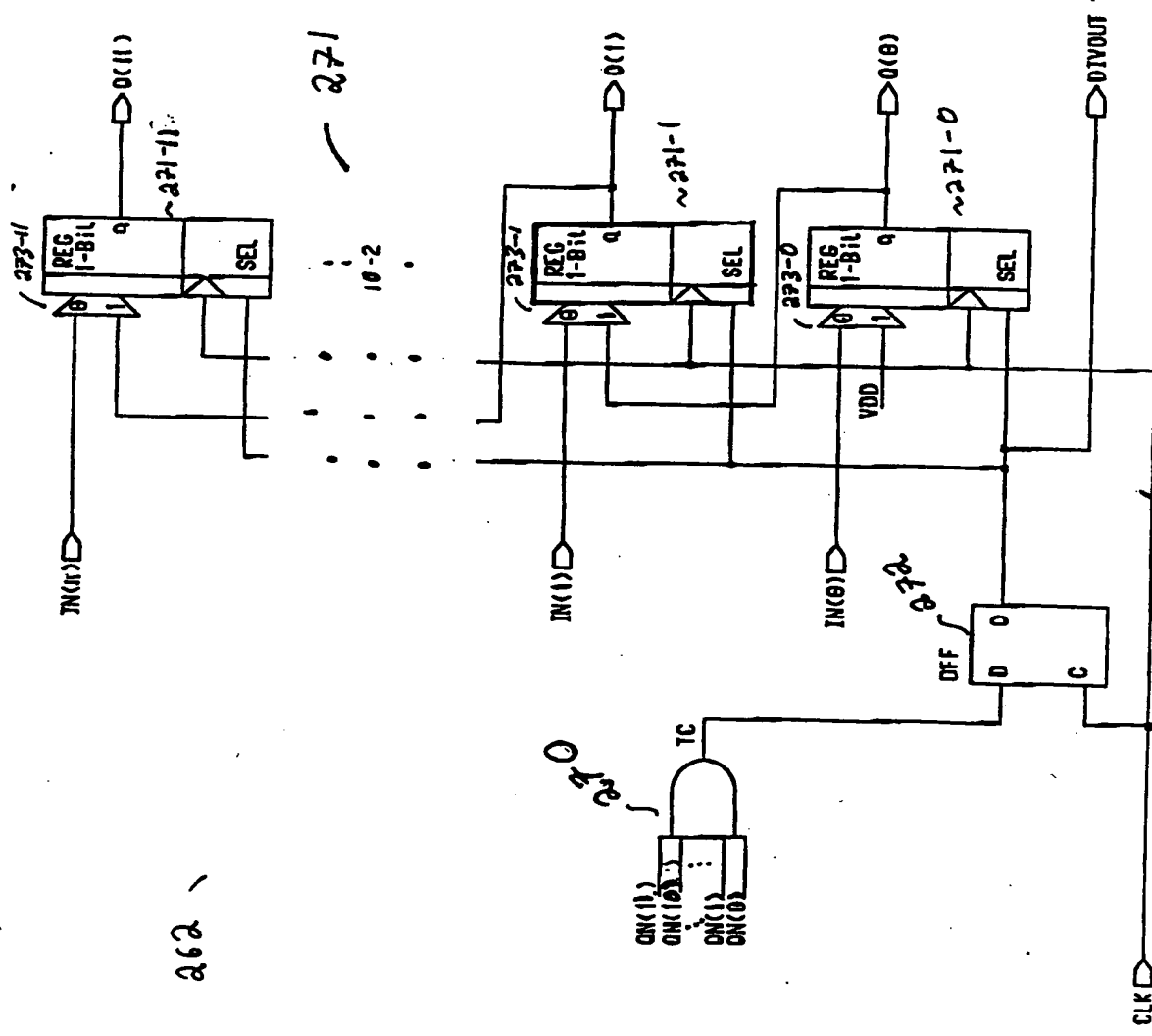


Figure 2h

243

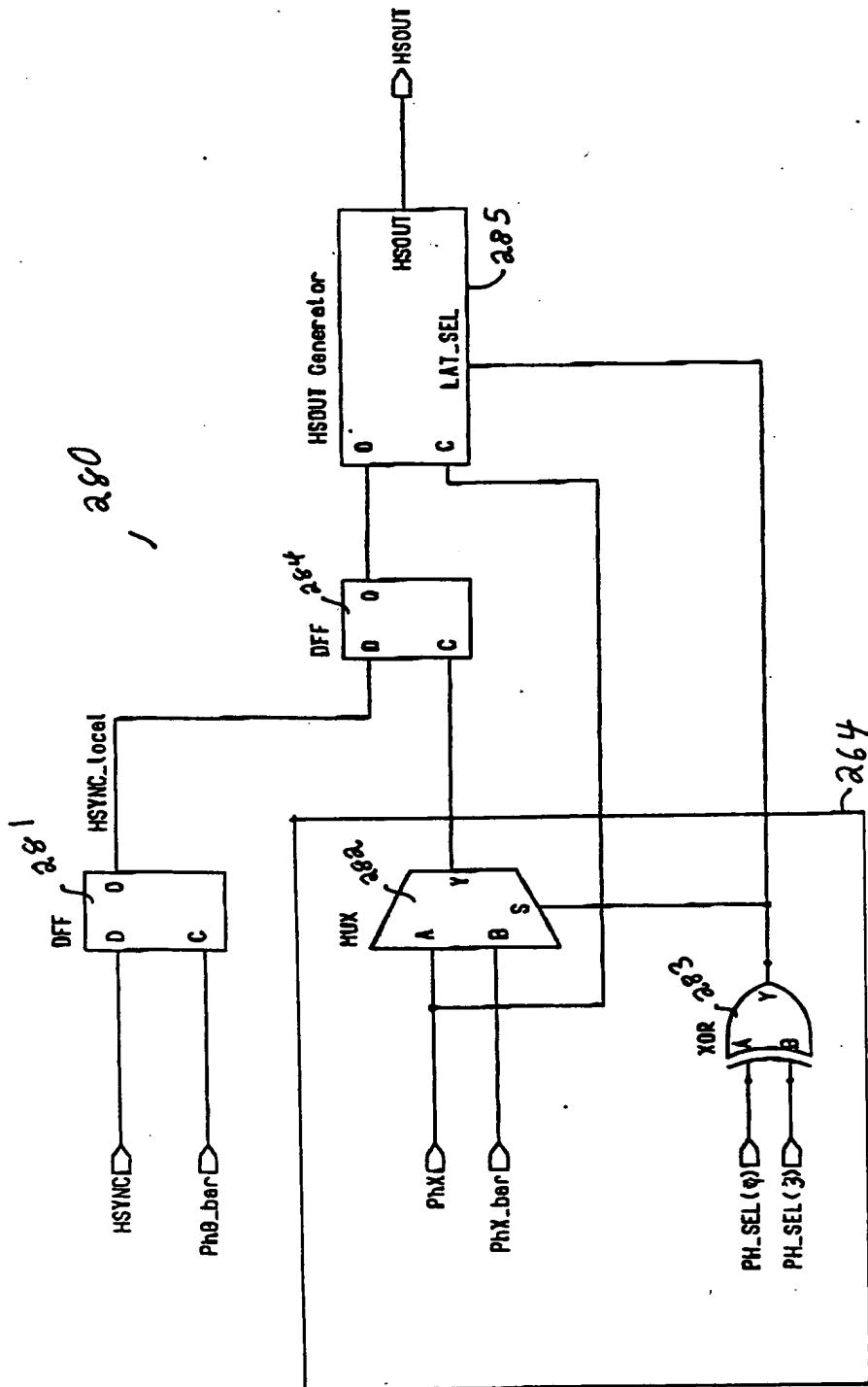


Figure 2.1

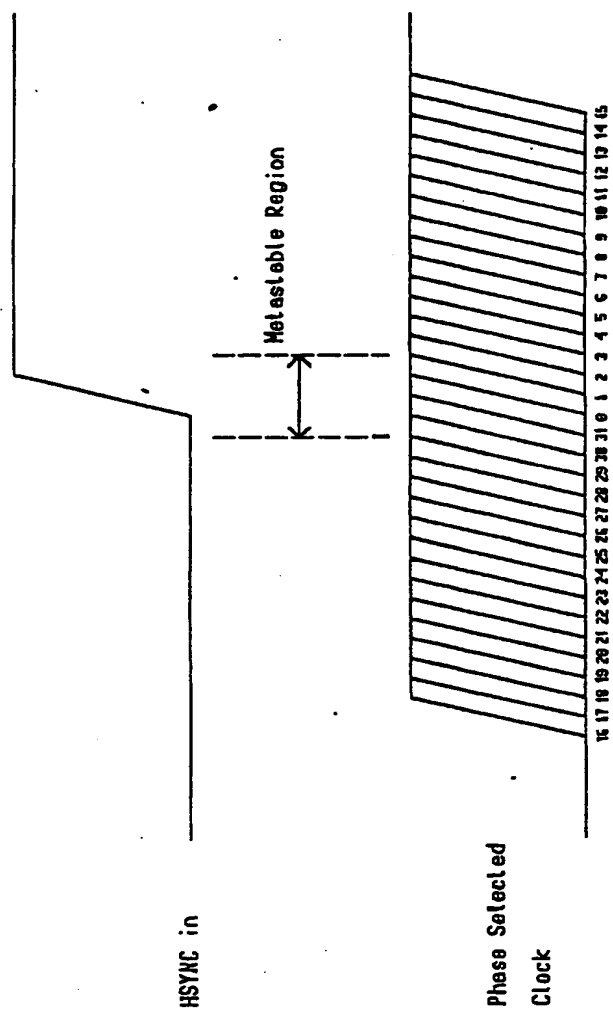


Figure 2j

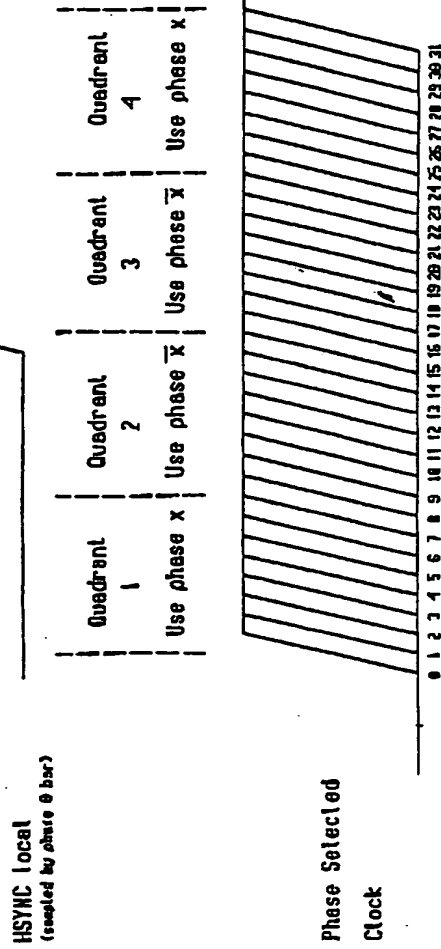
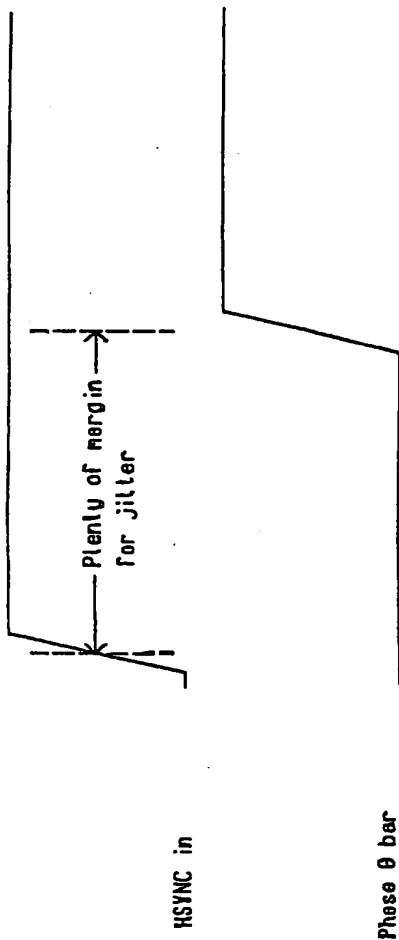
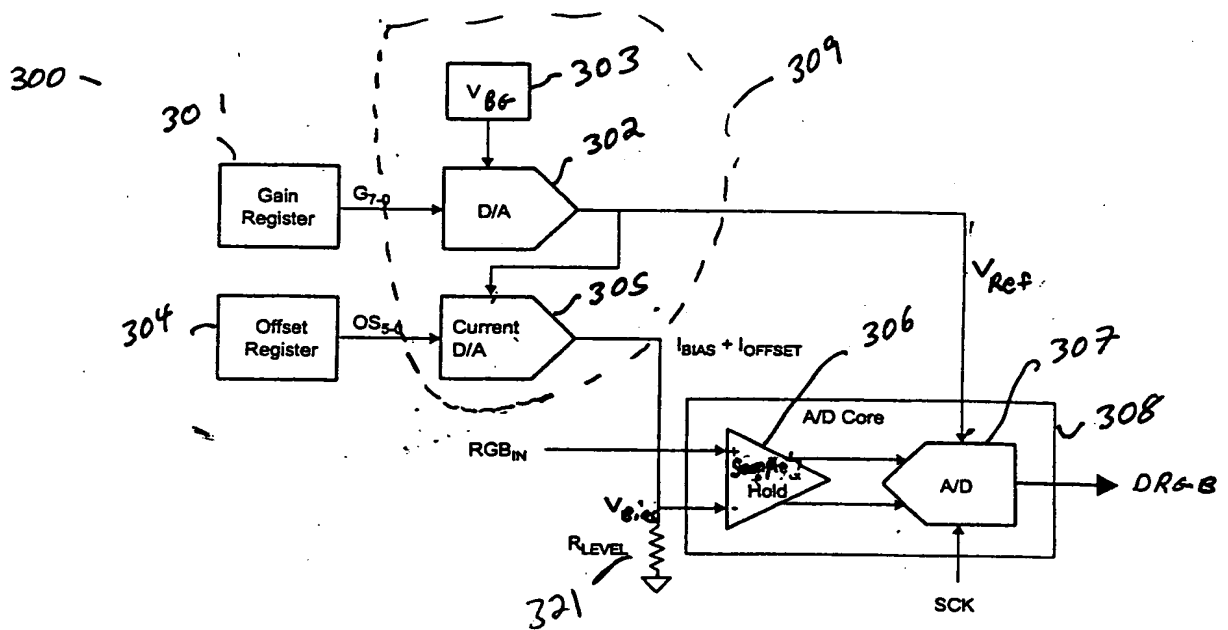
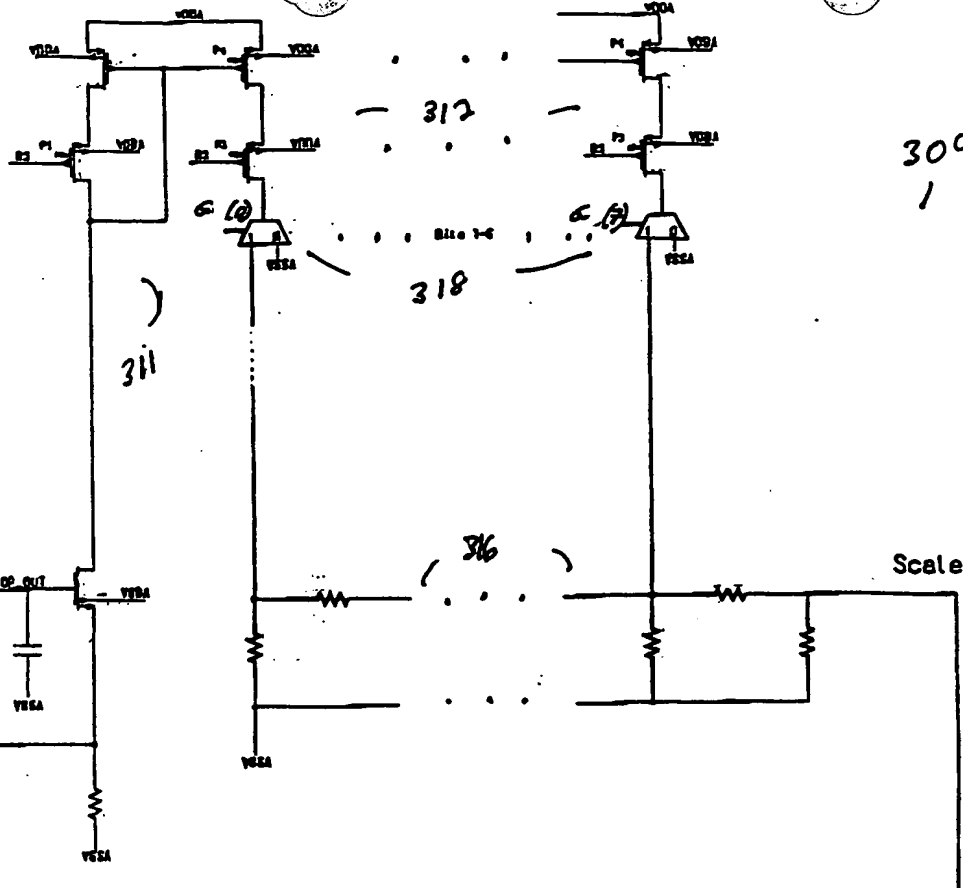
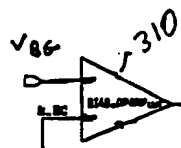


Figure 22

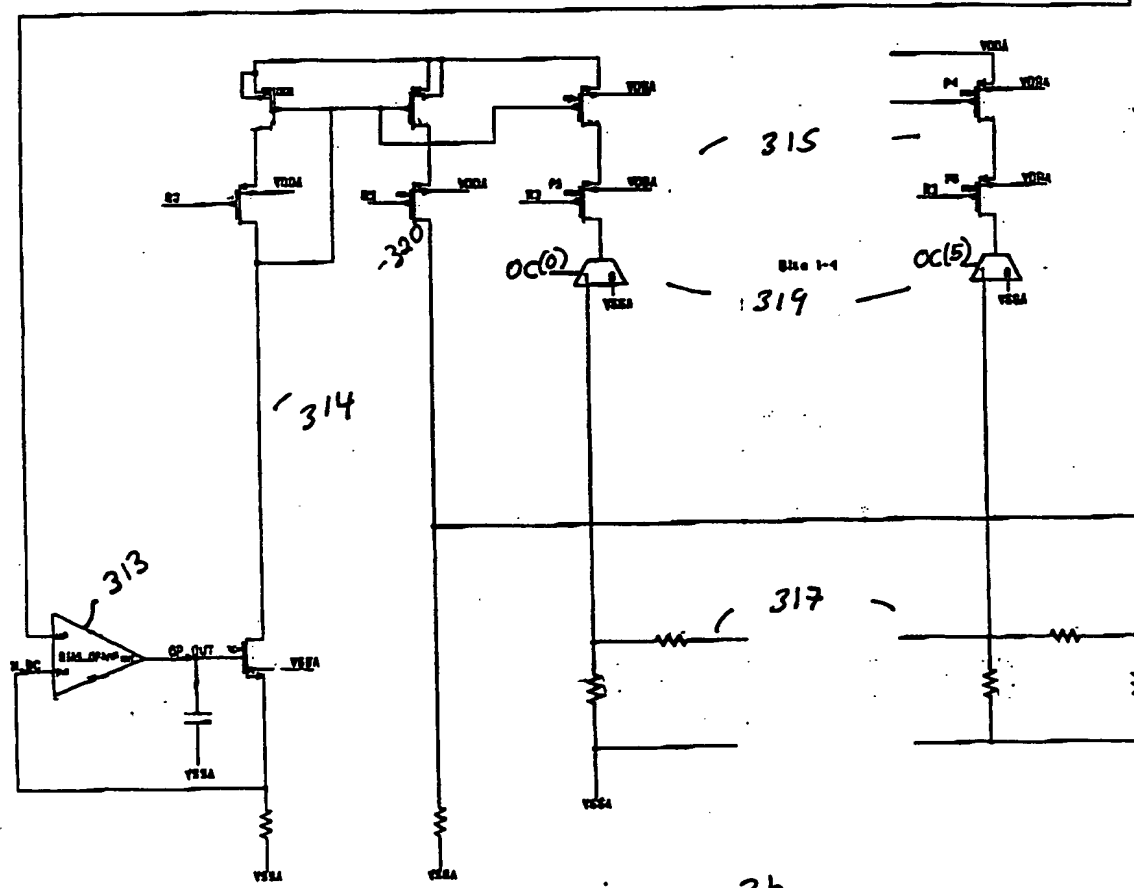


Figure 3a





Gain Control  
DAC  
8-Bits



Offset Control  
DAC  
6-Bits

Figure 3b

308

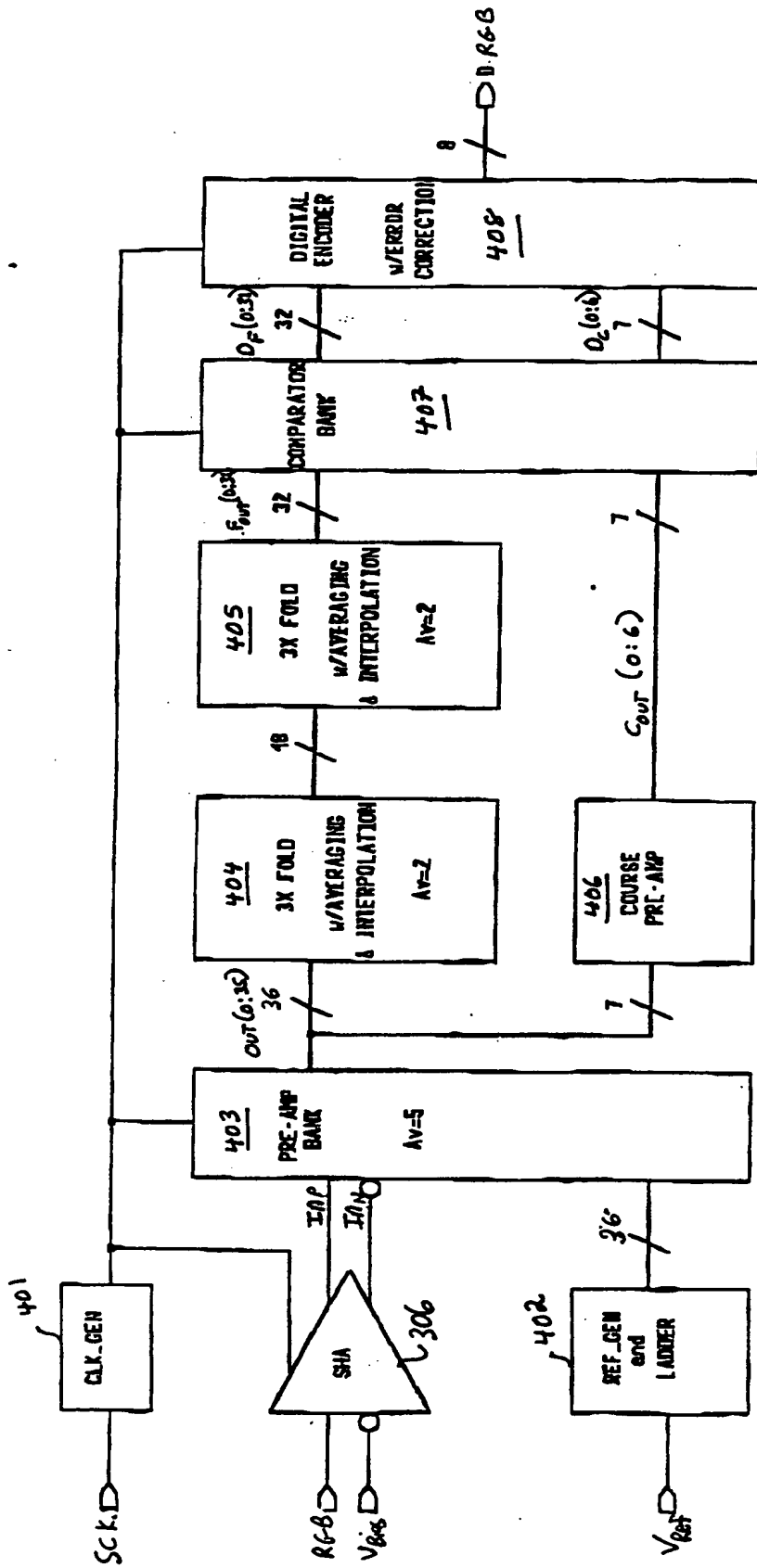


Figure 4a

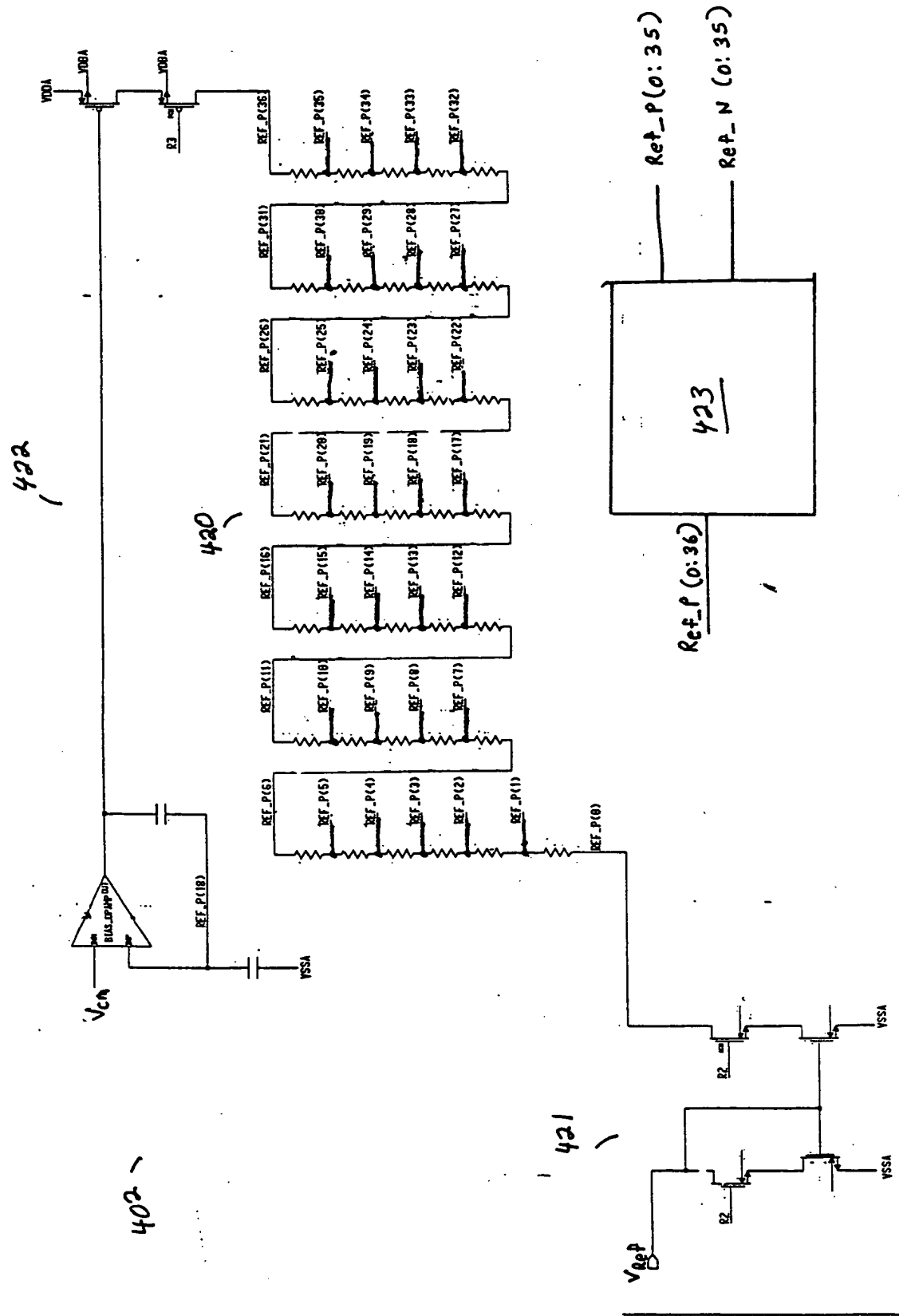


Figure 4b

306

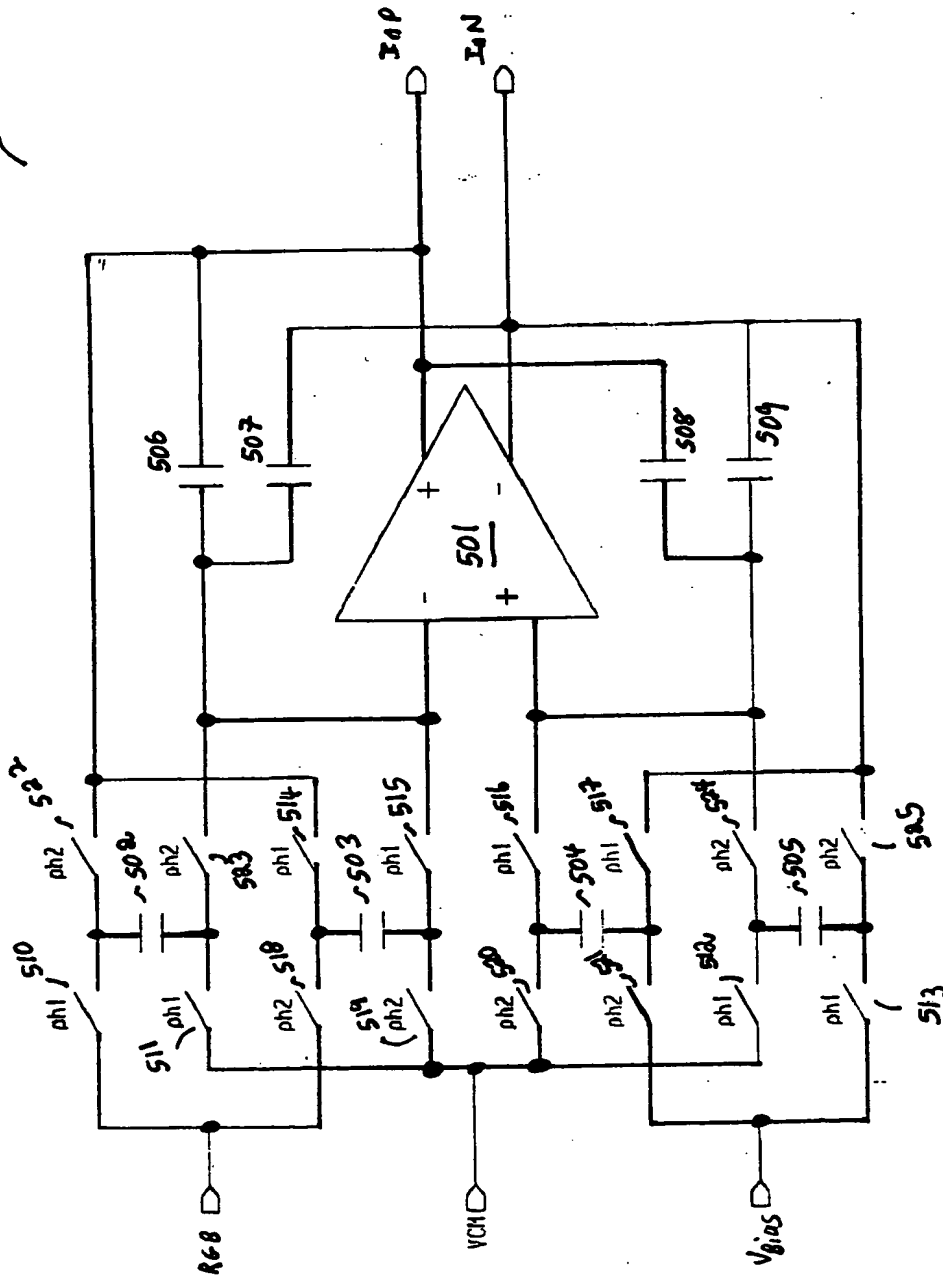
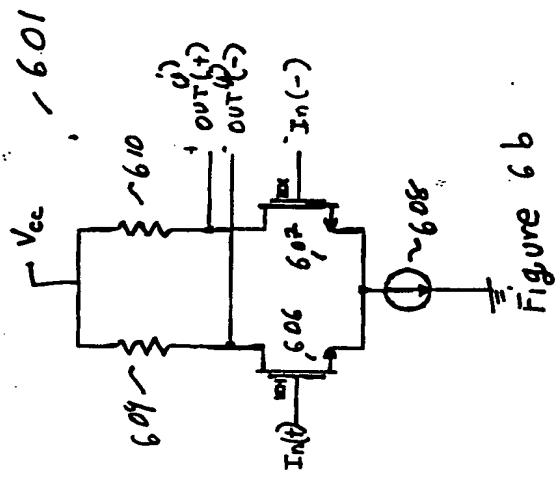


Figure 5



603

600

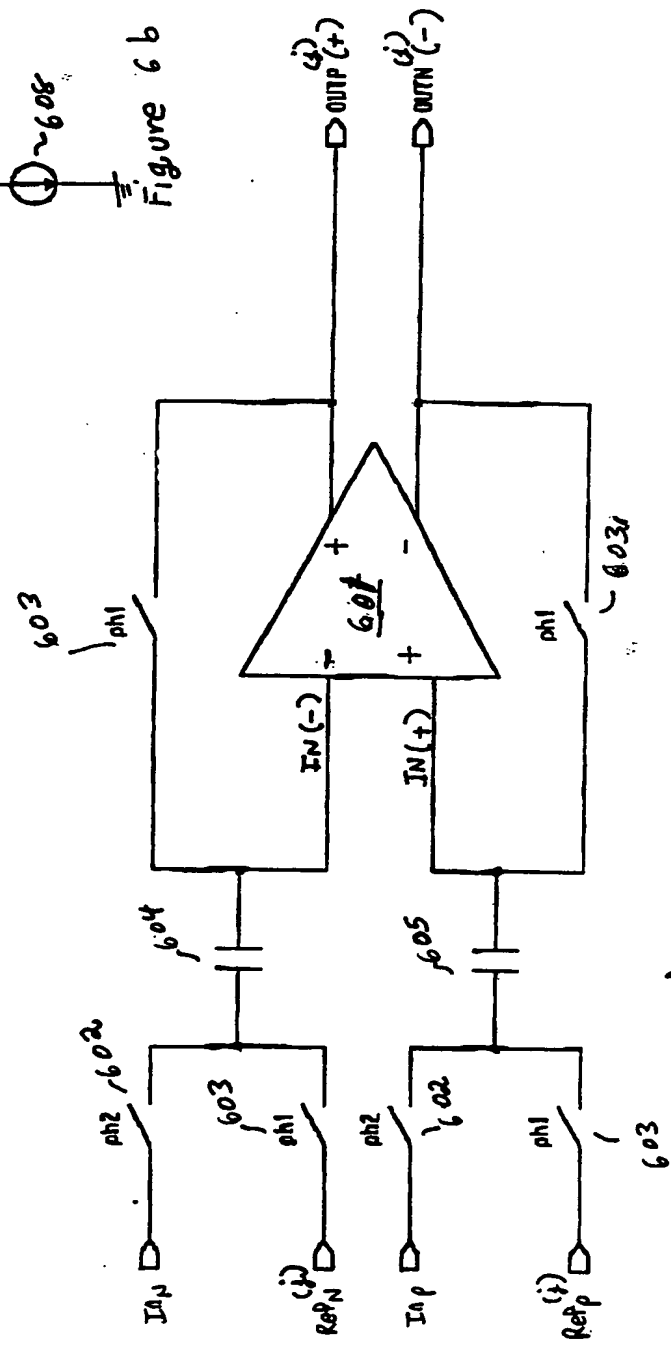


Figure 6a

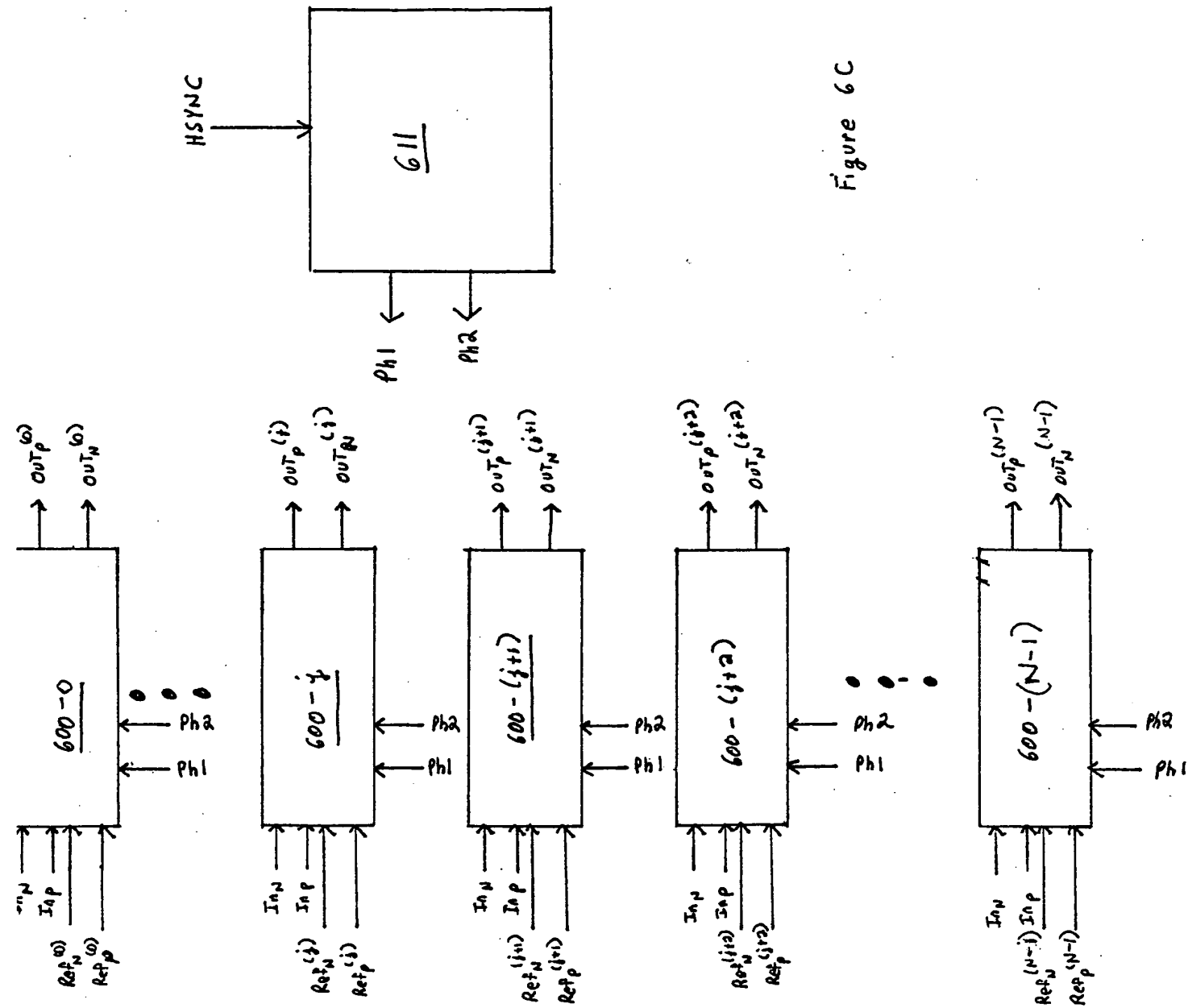
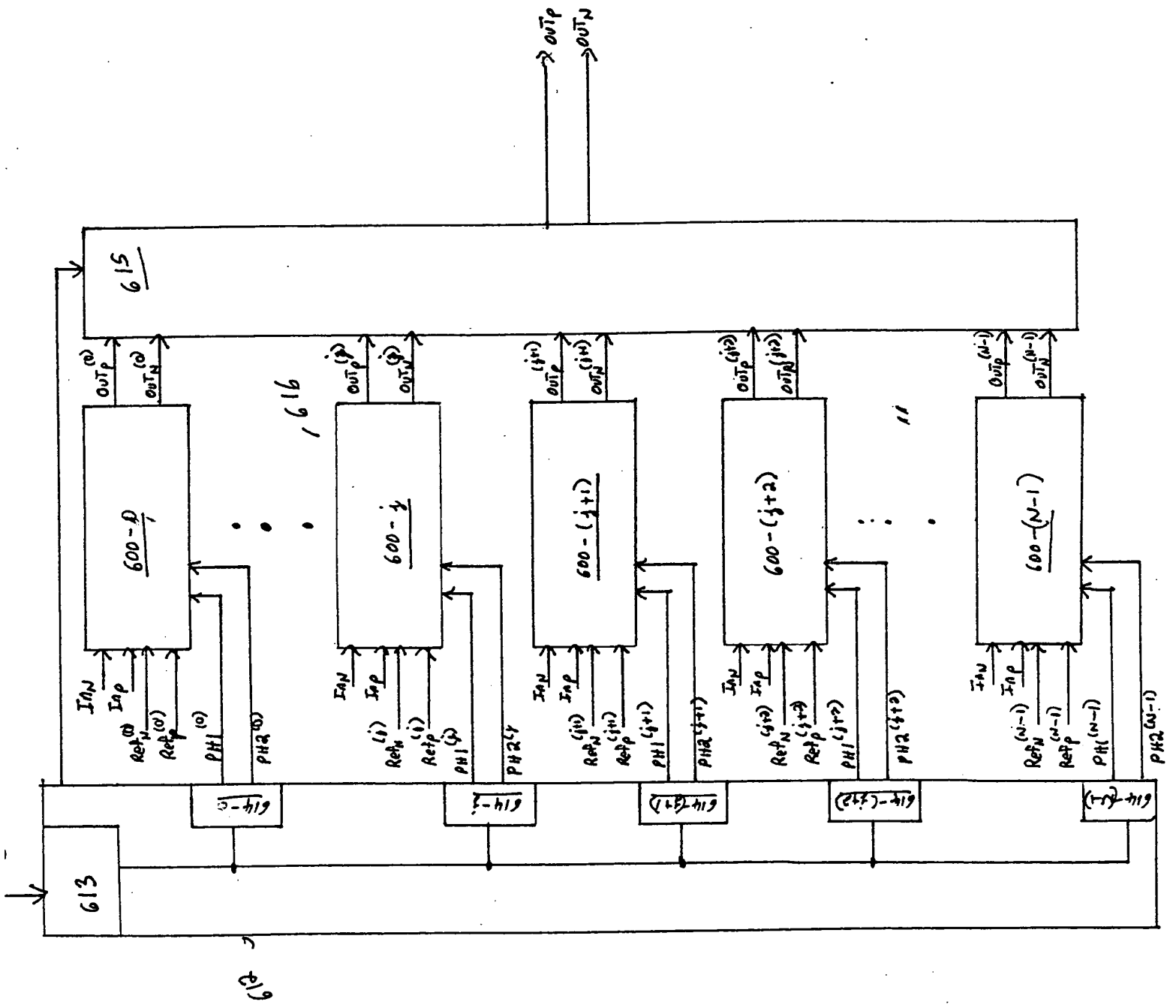


Figure 6C

Figure 60





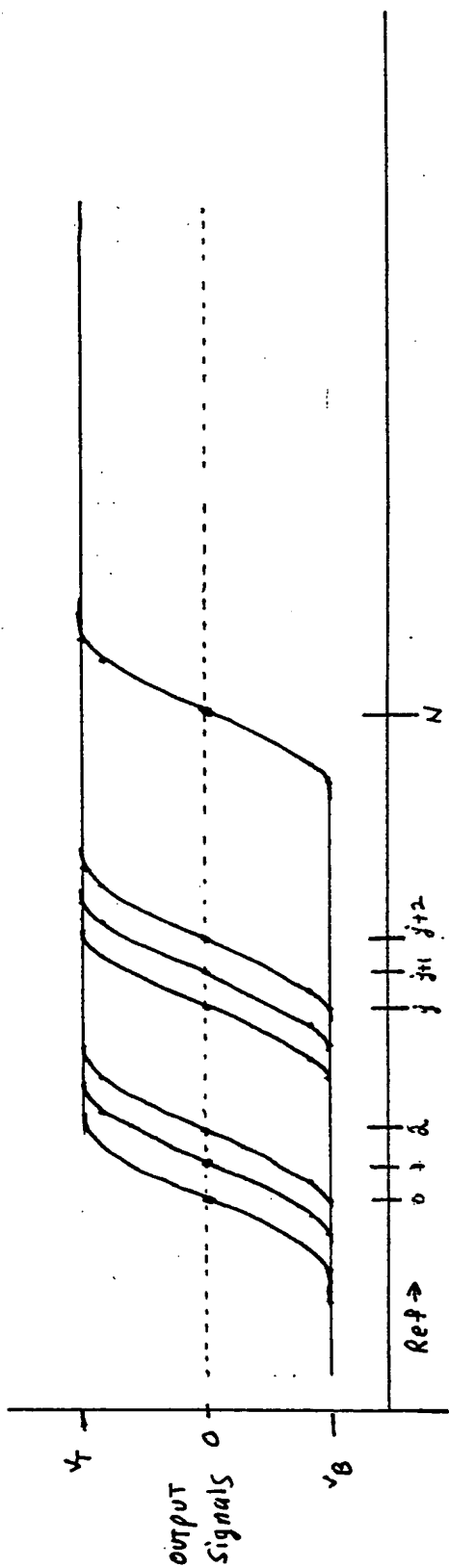


Figure 6E

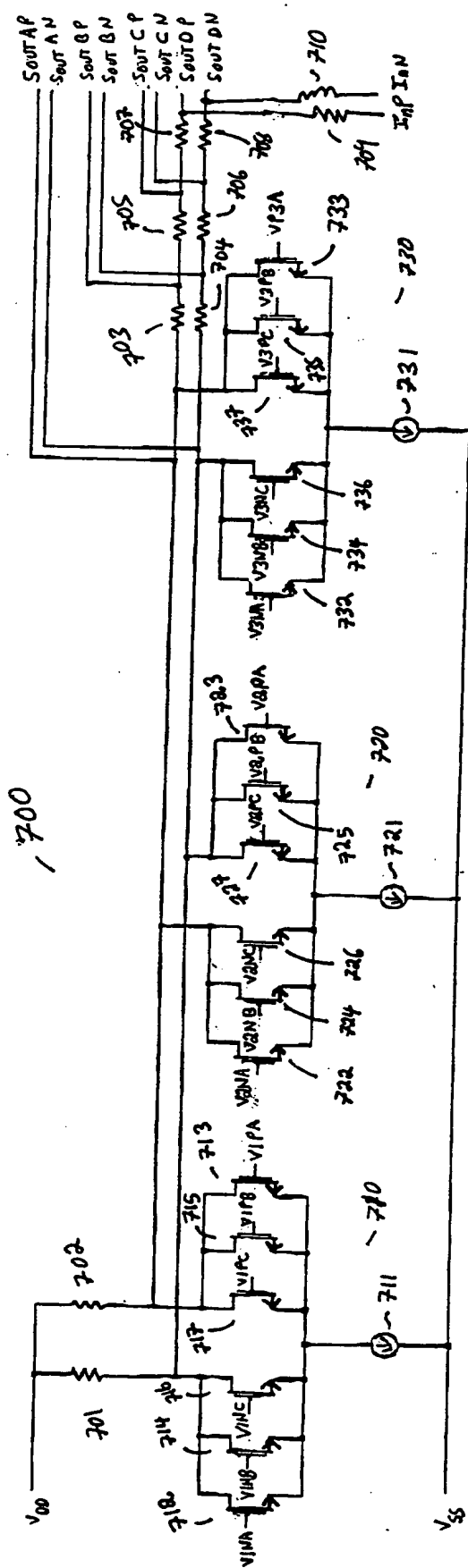


Figure 7a

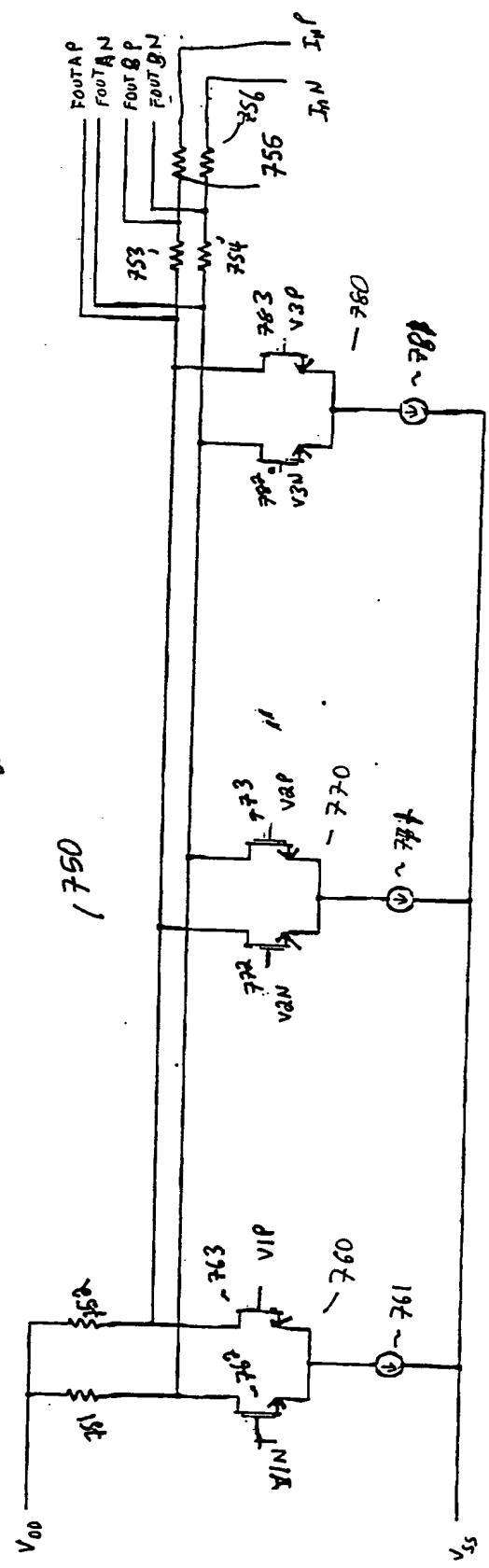
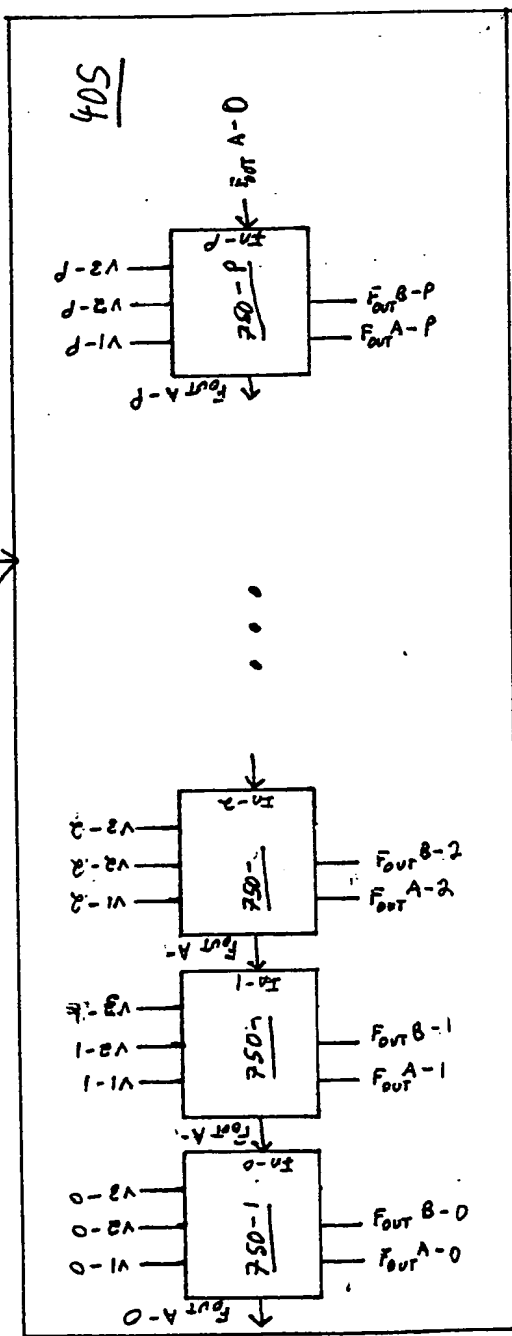
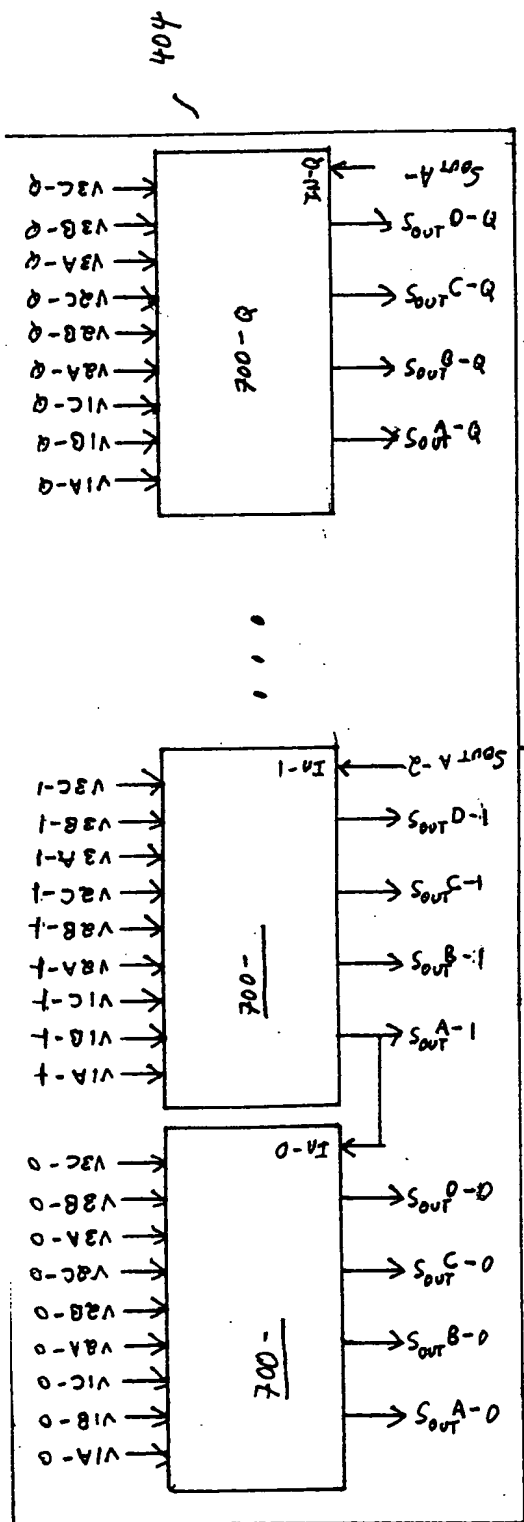


Figure 7b



$F_{OUT}$  (0:2P)

Figure 2C

BEST AVAILABLE COPY

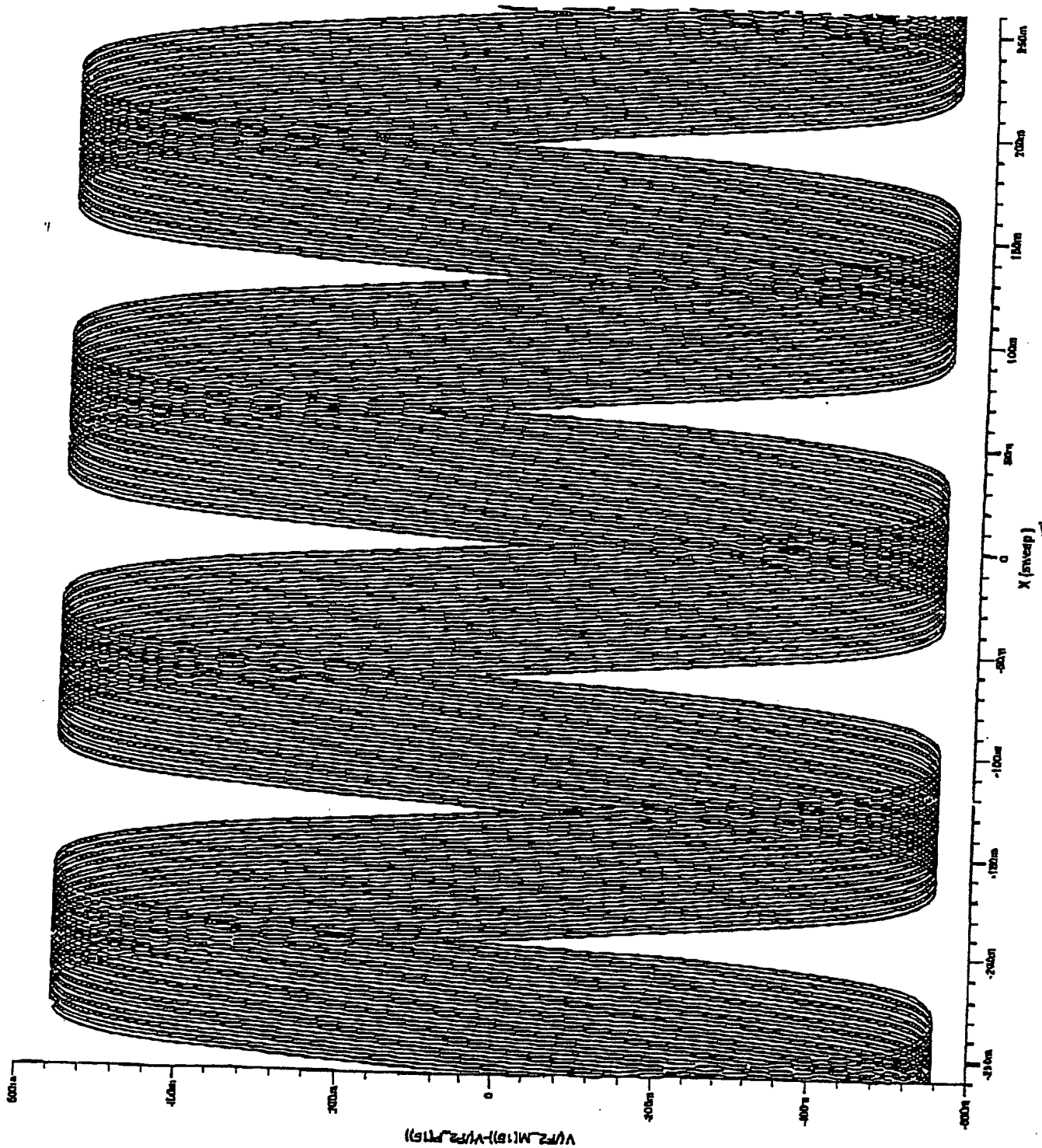


Figure 7D

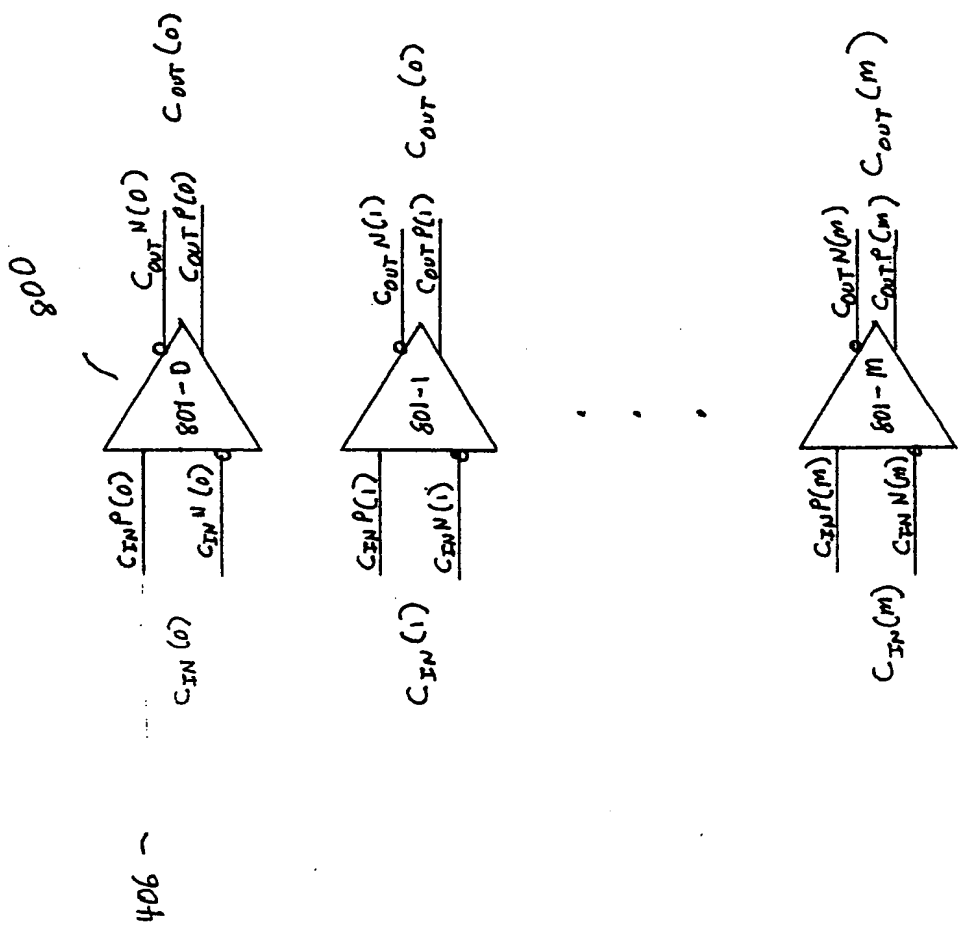


Figure 8

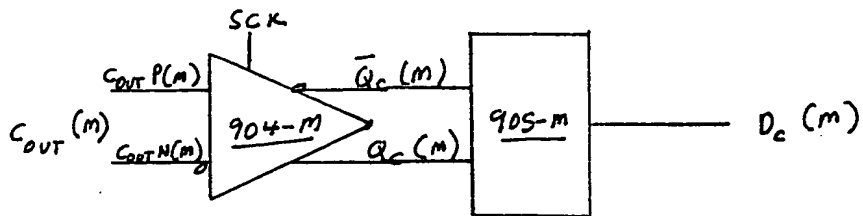
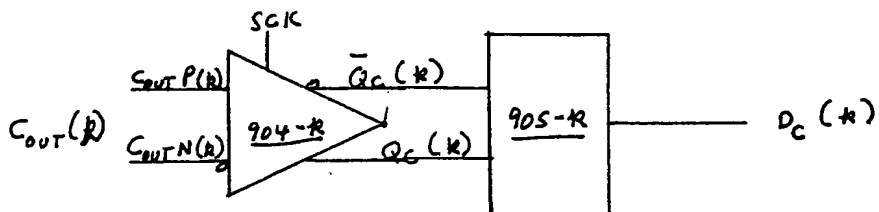
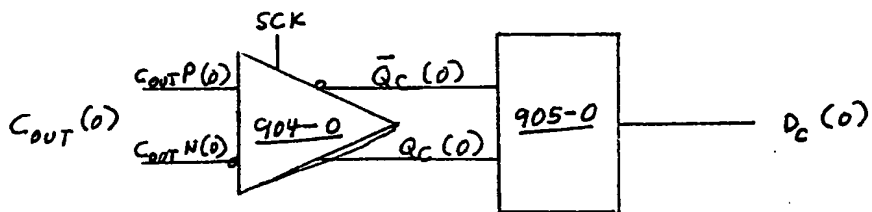
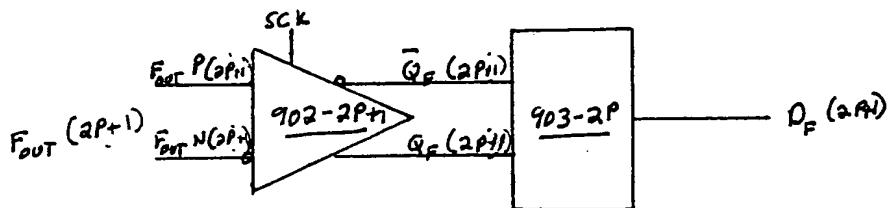
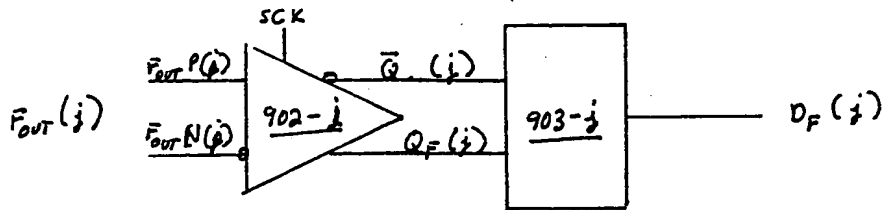
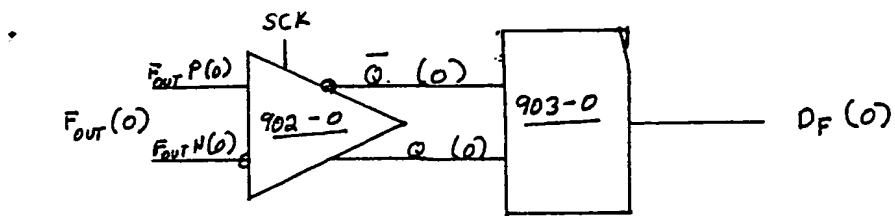


Figure 9

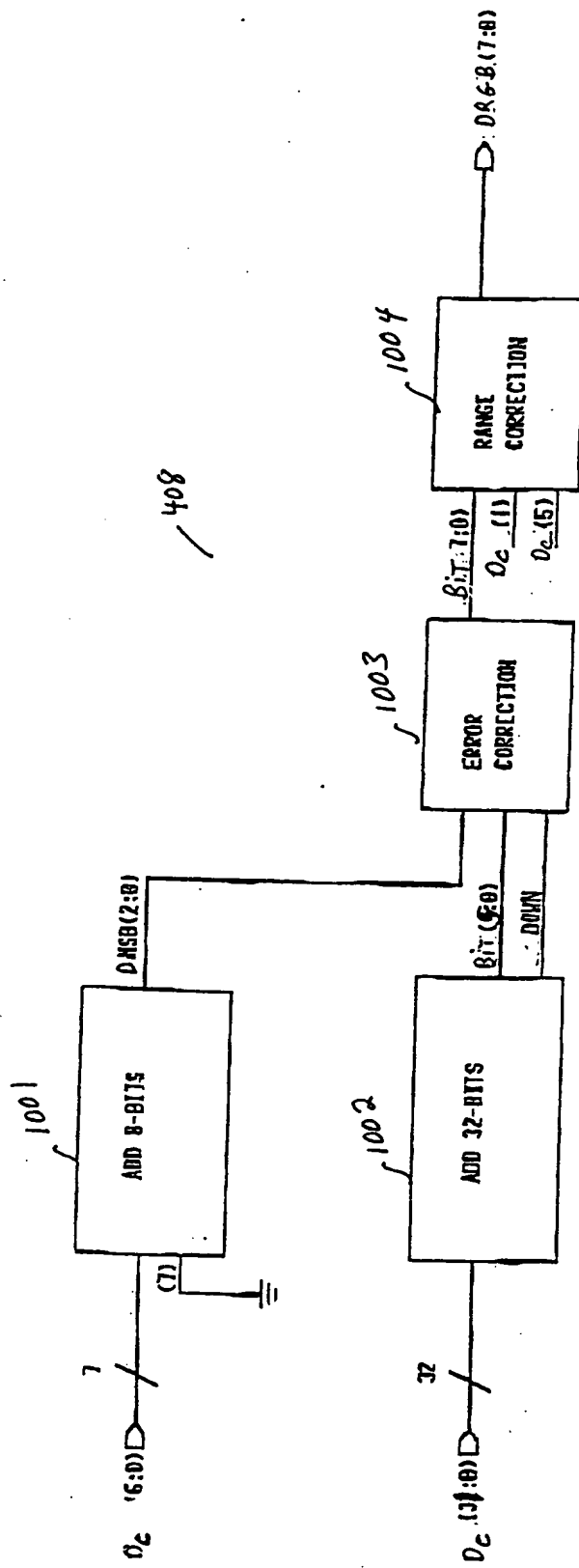


Figure 10a

